

DRAFT SHORT DATA SHEET: NXP.J2E081 (ID-1) on NXP Semiconductors SmartMX P5CD081 - MoTechno product# NXP.J2E081.1FF
 DRAFT SHORT DATA SHEET: NXP.J2E081 (ID-1/000 Plugin) on P5CD081 - MoTechno product# NXP.J2E081.2FF

<https://www.mot techno.com/buy/j2e081/> MoTechno product# NXP.J2E081

JCOP version: JCOP v2.4.2 R3

Specifications & features

JAVA CARD version 3.0.1 Classic

GlobalPlatform Card Specification 2.2.1

SCP SCP02 as default / SCP01 or SCP03 optional

Delegated management Yes

Secure box Yes

Available memory size and technology

EEPROM (KByte) 80

Implemented MIFARE options:

MIFARE Classic 1K / 4K / no MIFARE -

MIFARE FleXTM with MIFARE Classic 4K -

MIFARE DESFire EV1 8K -

eGovernment and banking performance

Basic Access Control (BAC) (20 kByte read) ask for an NDA (Info@MoTechno.com)

EAC (36 kByte read) ask for an NDA (Info@MoTechno.com)

MasterCard PayPass M/Chip4 (performance comparison) ask for an NDA (Info@MoTechno.com)

Cryptography

Data Encryption Standard (DES) and dual/triple key DES3 via coprocessor (on-card key generation):

DES/TDES [bit] 112/168

Advanced Encryption Standard (AES) via coprocessor (on-card key generation):

AES [bit] 128/192/256

PKI via coprocessor for RSA and ECC (on-card key generation):

RSA [bit] 1976 up to 2048

ECC GF(p) [bit] 128 up to 320

Point Addition (PACE) Yes

SHA SHA-1/2

Other cryptographic support SHA-1, SHA-224, SHA-256 and CRC

Certifications and approvals

Common Criteria (VAN5) EAL 5+

Supported communication interfaces

Contact mode: T=0 and T=1 according to ISO/IEC 7816 with 3.5712 MHz

Communication speeds: 38400 bit/s [default]
 9600 bit/s, 19200 bit/s, 57600 bit/s, 115200 bit/s and
 223200 bit/s [optional]

Requests for customized ATR are welcome.

Related products:

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